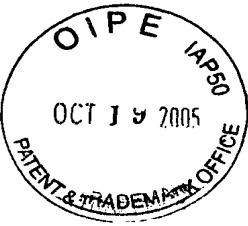


10-21-05

IFP



Express Mail No.: EV 533 715 848 US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of: Thomas Fox et al.

Confirmation No.: 7054

Application No.: 10/783,822

Art Unit: 2714

Filed: February 20, 2004

Examiner: Cao, Phat X.

For: *Stacked Semiconductor Module*

Attorney Docket No.: 60809-5001-US

RESPONSE TO RESTRICTION ELECTION

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This communication is responsive to the restriction requirement from the U.S. Patent and Trademark Office dated September 30, 2005. The period for reply is set to expire on October 30, 2005. Please enter the following election into the file of the above identified application.

RESTRICTION

The Examiner has imposed a restriction between:

Group I: Claims 1-43, drawn to processes of operation of a semiconductor module; and

Group II: Claims 44-47, drawn to a semiconductor module structure.

ELECTION AND TRAVERSE

Applicants elect Group I and Species Group A, without traverse.

CONCLUSION

No fees are believed due in connection with this response. However, the Commissioner is authorized to charge all required fees, fees under 37 C.F.R. § 1.17 and all required extension of time fees throughout the pendency of this application, or credit any overpayment, to Morgan, Lewis & Bockius LLP Deposit Account No. 50-0130 (order number 60809-5001-US).

Date October 19, 2005

Respectfully submitted,



45,645

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